

A vertical region designation circuit 141 outputs a vertical region designation signal to a vertical driver 103 on the basis of vertical display position information, a vertical synchronization signal and a horizontal synchronization signal. A horizontal region designation circuit 142 outputs a horizontal region designation signal to a horizontal driver 102 on the basis of horizontal display position information, a pixel synchronization signal, a vertical synchronization signal and a horizontal synchronization signal. The horizontal driver 102 outputs an input picture signal to a picture display surface 101 from a signal line at a horizontal coordinate corresponding to the number of times of pixel synchronization signals that is counted from an input horizontal synchronization signal as a starting point during a period when a horizontal region designation signal is effective. A vertical driver 103 stores the number of times of synchronization of a horizontal synchronization signal during a period when a vertical region designation signal is effective, and the vertical driver 103 selects a signal line at a vertical coordinate corresponding to the number of times of synchronization. The picture display surface 101 displays a picture signal output from the horizontal driver 102 at a position designated by a horizontal coordinate corresponding to the signal line where the picture signal was output and a vertical

coordinate corresponding to the signal line selected by  
the vertical driver 103.

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